

IN THE CLAIMS:

Please cancel Claims 1 and 2 without prejudice or disclaimer of the subject matter presented therein. The claims, as pending in the subject application, read as follows:

1 and 2. (Cancelled)

3. (Original) A semiconductor device manufacture method comprising:
a step of bonding a first substrate and a second substrate by using a spacer;

and

a step of cutting the first and second substrates,

wherein said step of cutting the first substrate cuts the first substrate at a position where the spacer is disposed under the first substrate.

4. (Original) A semiconductor device manufacture method according to claim 3, wherein the second substrate is formed with a light reception element.

5. (Original) A semiconductor device manufacture method according to claim 3, wherein the first substrate is formed with a plurality of lenses.

6. (Original) A semiconductor device manufacture method comprising:
a step of holding the semiconductor substrate on a base under a condition that the warp is removed;

a step of bonding an opposing substrate to the semiconductor substrates with a size adjusted according to the warp of the semiconductor substrate; and then a step of cutting the opposing substrate.

7. (Original) A semiconductor device manufacture method according to claim 6, comprising a step of bonding a plurality of opposing substrates to the semiconductor substrate with a gap of a plurality of opposing substrate corresponding to the size of the warp of the semiconductor substrate.

8. (Original) A semiconductor device manufacture method according to claim 6, wherein said step of bonding the opposing substrate to the semiconductor substrate uses a spacer disposed between the opposing substrate and the semiconductor substrate.

9. (Original) A semiconductor device manufacture method according to claim 6, wherein said step of cutting the opposing substrate cuts an area of the opposing substrate where a spacer is disposed under the opposing substrate.

10. (Original) A semiconductor device manufacture method according to claim 6, wherein the semiconductor substrate is formed with a light reception element or elements, and the opposing substrate is formed with an optical element or an optical element set for converging light on the light reception element or elements.

11. (Original) A semiconductor device manufacture method according to claim 6, wherein the opposing substrate is formed with a compound eye element having a plurality of lenses.

12. (Original) A semiconductor device manufacture method according to claim 6, wherein the semiconductor substrate is a semiconductor wafer.

13. (Original) A semiconductor device manufacture method according to claim 6, wherein the opposing substrate has a rectangular shape, a cross shape, a T-character shape, an I-character shape, an L-character shape or a polygonal shape.